



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Nevin et al. GROUP: 2823  
SERIAL NO: 10/699,503 EXAMINER: Hsien Ming Lee  
FILED: 10/31/03 Date of mailing  
of Notice of  
FOR: METHOD FOR FORMING A FILLED TRENCH Allowance: 12/30/05  
IN A SEMICONDUCTOR LAYER OF A SEMICONDUCTOR  
SUBSTRATE, AND A SEMICONDUCTOR SUBSTRATE  
WITH A SEMICONDUCTOR LAYER HAVING A FILLED  
TRENCH THEREIN

Mail Stop Issue Fee  
Assistant Commissioner of Patents  
P.O. Box 1450, Alexandria, VA 22313-1450

Attn: Official Draftsman

**TRANSMITTAL OF FORMAL DRAWINGS**

Applicant submits herewith new drawing(s) for this application. Attached please find eight (8) sheets of formal drawings for this application.

The three month period of response set in the Notice of Allowability (PTOL 37) expires on March 30, 2006, and this submission is on or before this expiry date.

Respectfully submitted,

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Issue Fee.

Deborah M. Costello

Date: 2-28-06